PART – B

(Short Essay)

Answer any 6 questions.

 $(6 \times 2 = 12)$

- 7. Write a short note on Three-State Bus Buffers.
- 8. Differentiate between instruction code and operation code.
- .9. Write a short note on Instruction cycle.
- 10. Differentiate between isolated and memory mapped I/O.
- 11. What is meant by interrupt cycle?

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- 12. Explain any two addressing modes of the CPU.
- 13. Differentiate between direct and indirect address modes.
- 14. Give any two interconnection structures for a multi-processor system.

PART - C

(Essay)

Answer any 4 questions.

 $(4 \times 3 = 12)$

- 15. Explain cache memory.
- 16. What are the advantage and disadvantage of three-address format?
- 17. Explain the basic operational concepts of a computer.
- 18. Explain stack organization.
- 19. Write a note on DMA.
- 20. Write a note on Asynchronous Data Transfer.

PART - D

(Long Essay)

Answer any 2 questions.

 $(2 \times 5 = 10)$

- 21. Explain any five memory reference instructions.
- 22. Explain the general register organization of a CPU.
- 23. Explain the various instruction formats with suitable examples.
- 24. Explain the memory hierarchy.