



K23U 0200

Reg. No. :

Name :

**VI Semester B.Sc. Degree (C.B.C.S.S. – Supplementary) Examination, April 2023
(2017 to 2018 Admissions)
CORE COURSE IN COMPUTER SCIENCE
6B15CSC : Computer Organization**

Time : 3 Hours

Max. Marks : 40

SECTION – A

1. One word answer :

(8×0.5=4)

- a) ISR stands for _____
- b) The transfer of new information to be stored into memory is called _____
- c) The interrupt enable flip-flop is cleared to zero using _____
- d) One address instructions use _____ register for all data manipulation.
- e) ROL is an example for _____ instruction.
- f) Daisywheel is a _____ printer.
- g) UART stands for _____
- h) _____ loads the OS from disk to main memory and transfer the control to the OS.

SECTION – B

Write short notes on **any seven of the following questions :**

(7×2=14)

2. What are supercomputers ?
3. What do you mean by interrupt ?
4. Write in brief about instruction code.
5. Explain :
 - a) BUN
 - b) BSA.

P.T.O.



6. What is a control memory ?
7. Why subroutines are used ?
8. What do you mean by timeout in data transfer ?
9. Write about ASCII.
10. Distinguish between tightly and loosely coupled system.
11. What do you mean by write-through method ?

SECTION – C

Answer **any four** of the following questions :

(4×3=12)

12. Describe in detail about bus.
13. Explain the interrupt cycle.
14. Distinguish between direct and indirect address.
15. Write short essay about zero address instructions.
16. Explain any six logical and bit manipulation instructions.
17. Give details about any two auxiliary storage devices.

SECTION – D

Answer an essay on **any two** of the following questions :

(2×5=10)

18. Write an essay on the different phases in instruction cycle.
 19. Describe the various data transfer and manipulation instructions.
 20. Explain the strobe and handshaking method in asynchronous data transfer.
 21. What do you mean by a multiprocessor system ? Explain the different interconnection structures.
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